

Substitute for Form 1449 A & B/PTO <u>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</u> <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	Not Yet Assigned
				Confirmation Number	Not Yet Assigned
				Filing Date	December 19, 2005
				First Named Inventor	Kenichi SHIMURA
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	Q92193

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US 2003/0111626	A1	06-19-2003	
		US 6,211,090	B1	04-03-2001	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	2002-289807	A	10-11-2002		
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		Durlam M. et al. "A LOW POWER 1 MBIT MRAM BASED ON 1T1MTJ BIT CELL INTEGRATED WITH COPPER INTERCONNECTS," 2002 Symposium on VLSI Circuits Digest of Technical Papers, pages 158 to 161.	

Examiner Signature		Date Considered	
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